



• D2SX Series 2.5*2.0 OSC



FEATURES

- 2.5X2.0X0.9 Miniature Package
- Tri-State Enable/Disable
- TTL/CMOS compatible
- Home security devices, power saving application, and low frequency specification circuit design looking for a smaller package
- 3.3V, 2.8V(2.5V), 1.8V,1.5V option

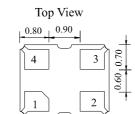
Electrical Specifications

Deremeter		D	CV.			
Parameter		Condition	D2SX			
Frequency Range	F0		1~125MHz			
Frequency Stability*		All Condition	± 25 ppm, ± 50 ppm, ± 100 ppm			
Operating Temperature Range	Topr		-20°C~+70°C(-40°C~+85°C option)			
Storage Temperature Range	Tstg		-55℃~+125℃			
Power supply Voltage	V_{DD}		3.3V+/-10%	2.8V+/-10%	1.8V+/-10%	$1.5V \pm 10\%$
Supply Current	Idd	1MHz to 9.999MHz	8mA Max	7mA Max	6mA Max	5mA Max
		10MHz to 34.999MHz	10mA Max	8mA Max	7mA Max	6mA Max
		35MHz to 49.999MHz	25mA Max	20mA Max	15mA Max	15mA Max
		50MHz to 125.00MHz	35mA Max	30mA Max	25mA Max	25mA Max
Output Symmetry	Sym	At 1/2V _{DD}	40/60%(45/55% Option)			
Rise time	Tr	10%Vdd~ $90%$ Vdd	5 nS Max	6 nS Max	7 nS Max	7 nS Max
Fall Time	$T_{\rm f}$	90%Vdd~ $10%$ Vdd	5 nS Max	6 nS Max	7 nS Max	7 nS Max
Output Voltage	Voh		90% Vdd Min			
	Vol		10% Vdd Max			
Output Load HCMOS Load			15рF Тур.			
Start Time	Ts		10mS Max			
Stand-by Function				Ye	es	
Aging(First Year)		25℃±3℃		±2ppr	n Max	
Pin 1,tri-state function			Pin 1=H or openOutput active at pin 3			
			Pin 1=Lhigh impedance at pin 3			
Packing Unit		3000pcs/reel				

*Include: 25° C tolerance, operating temperature range, input voltage change, aging, load change, shock and vibration

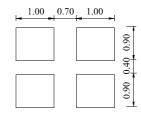
Mechanical Dimensions(mm)





Pin	Connection
#1	Tri-State
#2	GND
#3	Output
#4	Vdd

Recommended Solder Pattern



**Note: 0.01uF bypass capacitor should be placed between VDD(Pin4) and GND(Pin2) to Minimize power supply line noise